



Dipartimento di Ingegneria Elettrica e
delle Tecnologie dell'Informazione
Corso di Risk Assessment

AVVISO DI SEMINARIO

Risk assessment in real life: experiences from the railway domain

by

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HITACHI RAIL STS

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Abstract:

A process of hazard identification and analysis in a real context of metro project is presented, focusing on techniques used by RAMS teams for the risk assessment of multi-level systems. The lecture combines insights into the fundamentals of an integrated risk assessment approach applied to railway systems, based on the current development life cycle and standards (CENELEC, CSM-RA), and practical hints on conducting concrete analyses through the most used qualitative and quantitative techniques, including Checklists, HAZOP, FTA. In particular, experiences on explicit risk estimation are described in details with the objective of bringing the introduced concepts in the industrial practice and environment.

Biographic sketches:

Emilia Di Martino was born in Vico Equense (Naples) in 1982. She received the master's degree (2008) in Management Engineering from University of Naples "Federico II". From 2008 she has been working in Hitachi STS in RAMS department as System RAMS Engineer. She is currently RAMS System Integrator for Glasgow Metro and Ethiad Railway projects.

Diego Gerbasio was born in Salerno (Italy) in 1981. He received the master's degree in Electronic Engineering (2007) and the Ph.D. in Information Engineering (2016) from the University of Salerno (Italy). From 2008 to 2015 he worked as research assistant at University of Salerno and published papers on Robotics, Industrial Automation and Discrete Event Systems. He has been working in Hitachi STS from 2016 in RAMS department as Senior Signalling RAMS Engineer. He is currently RAMS Signalling Integrator of Programme Modernisation Metro Bruxelles and Signalling Safety Engineer for Generic CBTC Solutions.

Claudio Mazzariello graduated in 2004 in Telecommunications Engineering (MSc) and in 2007 received his Doctoral Degree (PhD) in Computer and Automation Engineering from the University of Napoli (Italy). His research interest encompassed artificial intelligence, network security and critical infrastructure protection, in 2011 he joined Ansaldo STS, (currently Hitachi Rail STS), where he has worked as software developer, safety engineer, as Project Safety Manager in Turnkey Metro projects. He is currently Project Manager for the supply of a Traffic Management and Control System to Hyperloop Transportation Technologies, and Head of the Innovation Management Office within the Digital, Innovation & Lumada Department.

Aniello Paolillo was born in Avellino (Italy) in 1983. He received the master's degree in Management Engineering (2008) from the Federico II University of Naples (Italy). He has been working in Hitachi STS from 2008 in the RAMS department: from 2008 to 2015 - System and Signalling RAMS Engineer on Roma Linea C, Riyadh, Brescia, Milano Linea 5 driverless metro projects; from 2014 to present - Senior Project RAMS Manager on Aarhus Light Rail and Glasgow Subway Modernization projects.

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